

Figure 1: System Architecture

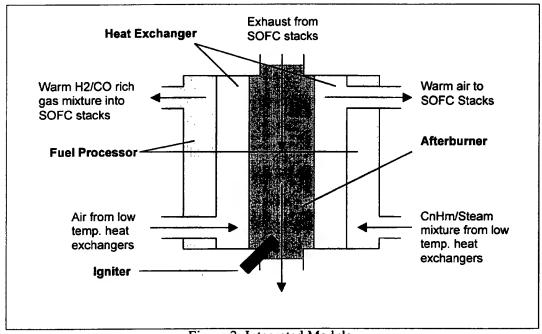


Figure 2: Integrated Module